

A proof of concept for IEEE P1581

P1581 Demo Board discussion and demonstration

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Objective

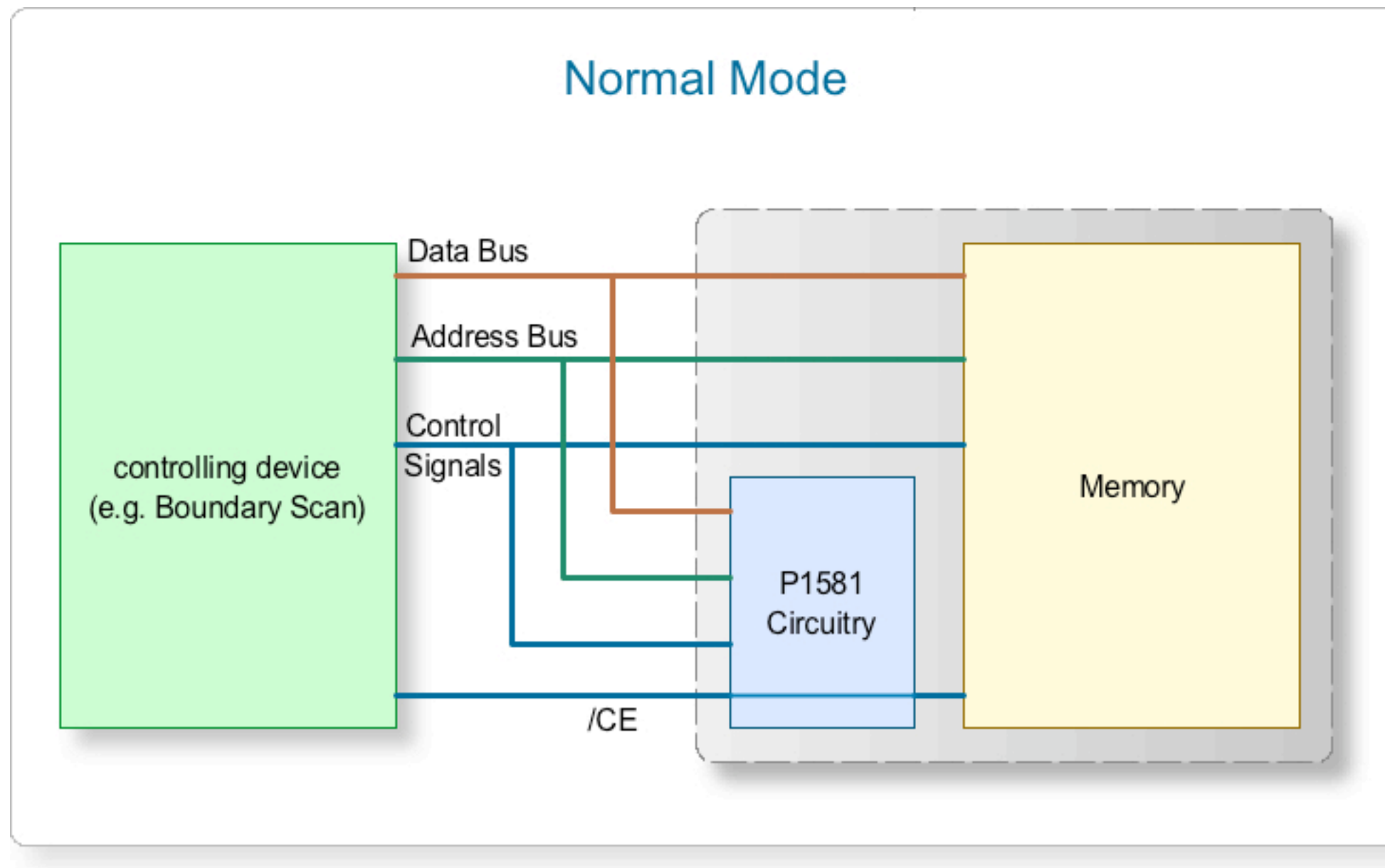
- Background Information on IEEE P1581
- Introduction of P1581 Demo Board
- Compare Test Methods
- Solicit Feedback

Disclaimer: P1581 Working Group may or may not agree with content.

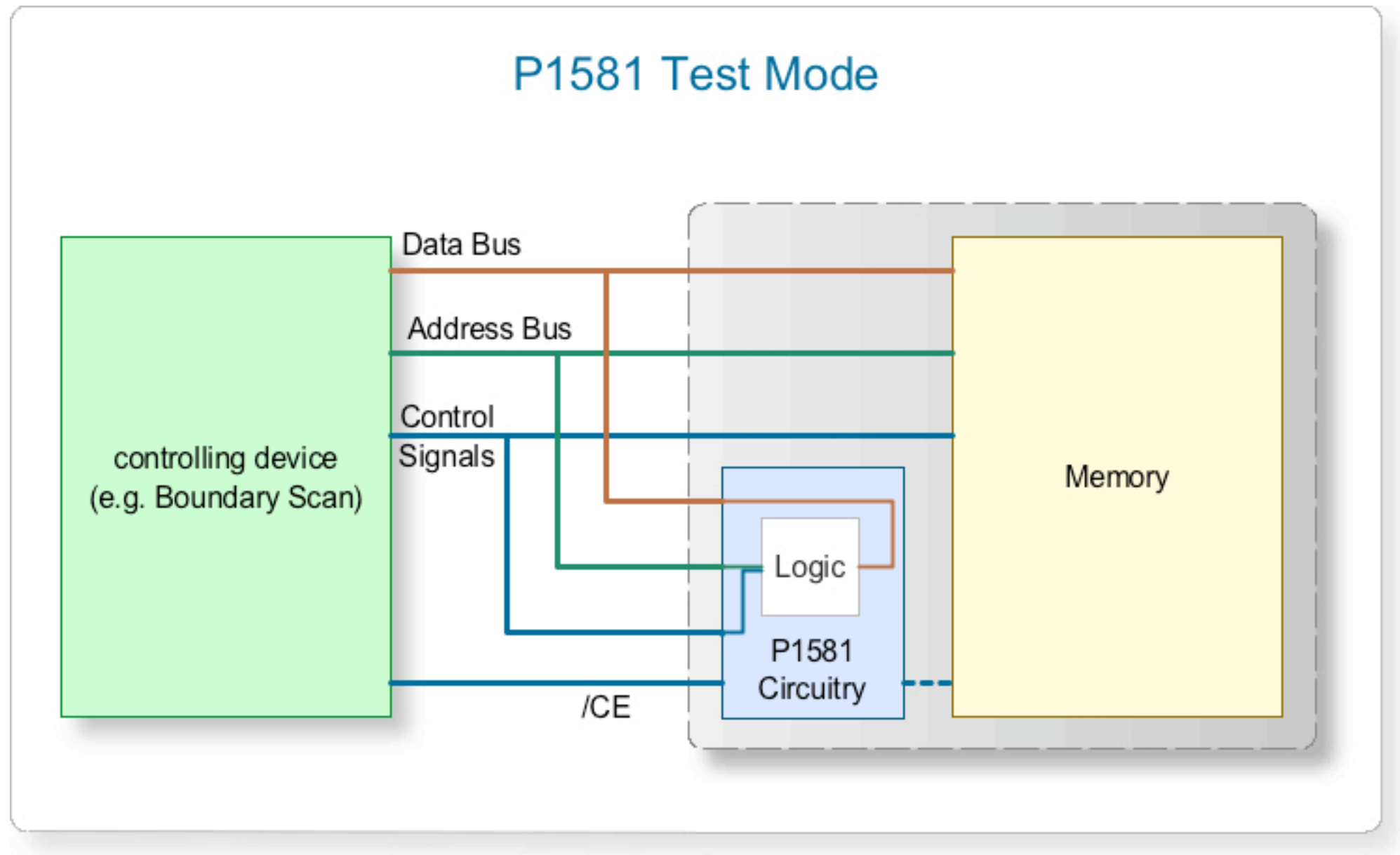
PI581 – A quick review

- Simple Test Logic Implementation for Memory Devices *(and possibly other complex, slave-type components)*
- No extra pins required
- Not relying on complex Memory Access Cycles
- Fast test execution, small test vector set
- Usable with any access methodology (BScan, functional, even ICT)

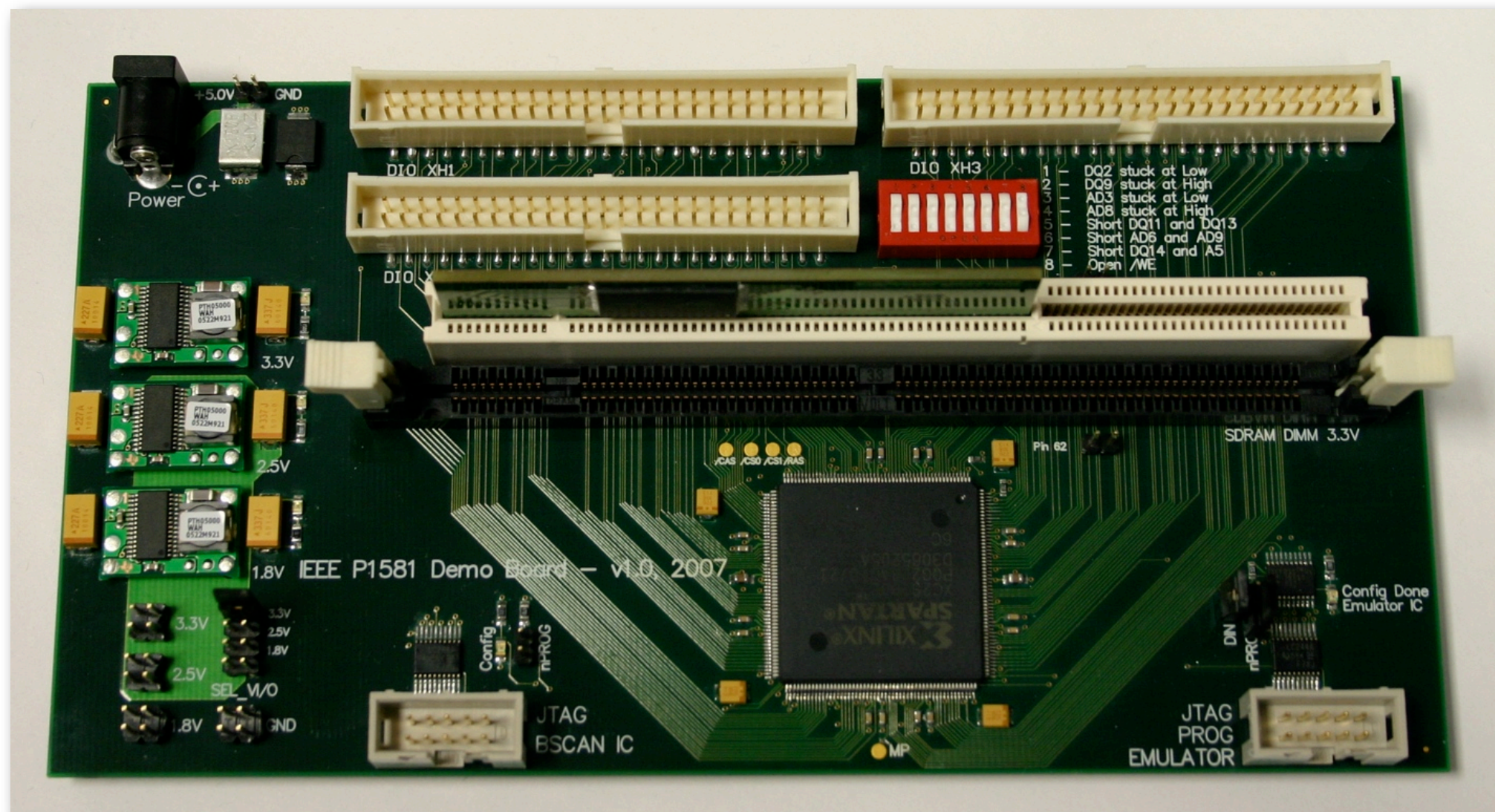
P1581 Concept



P1581 Concept



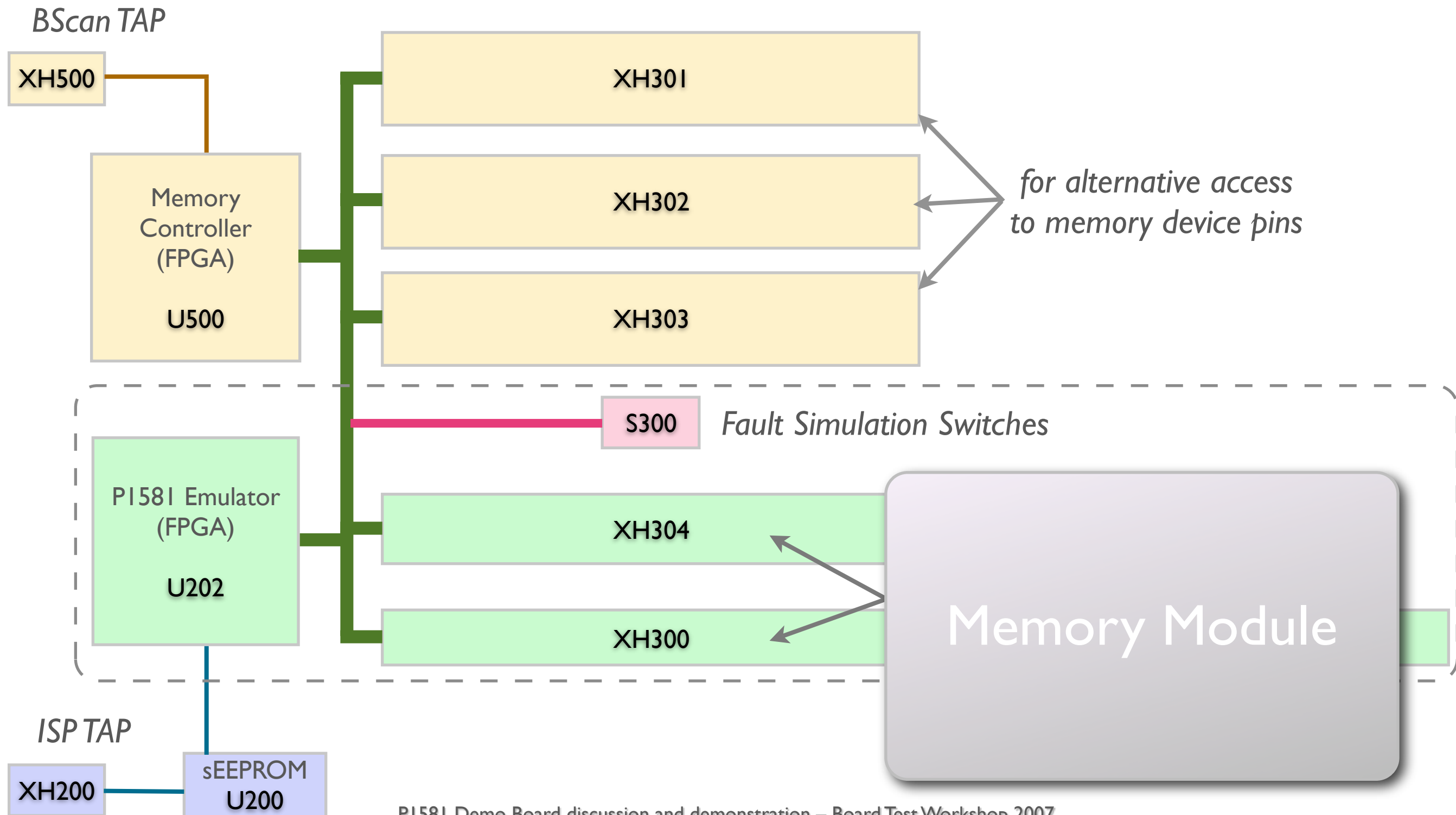
P1581 Demo Board



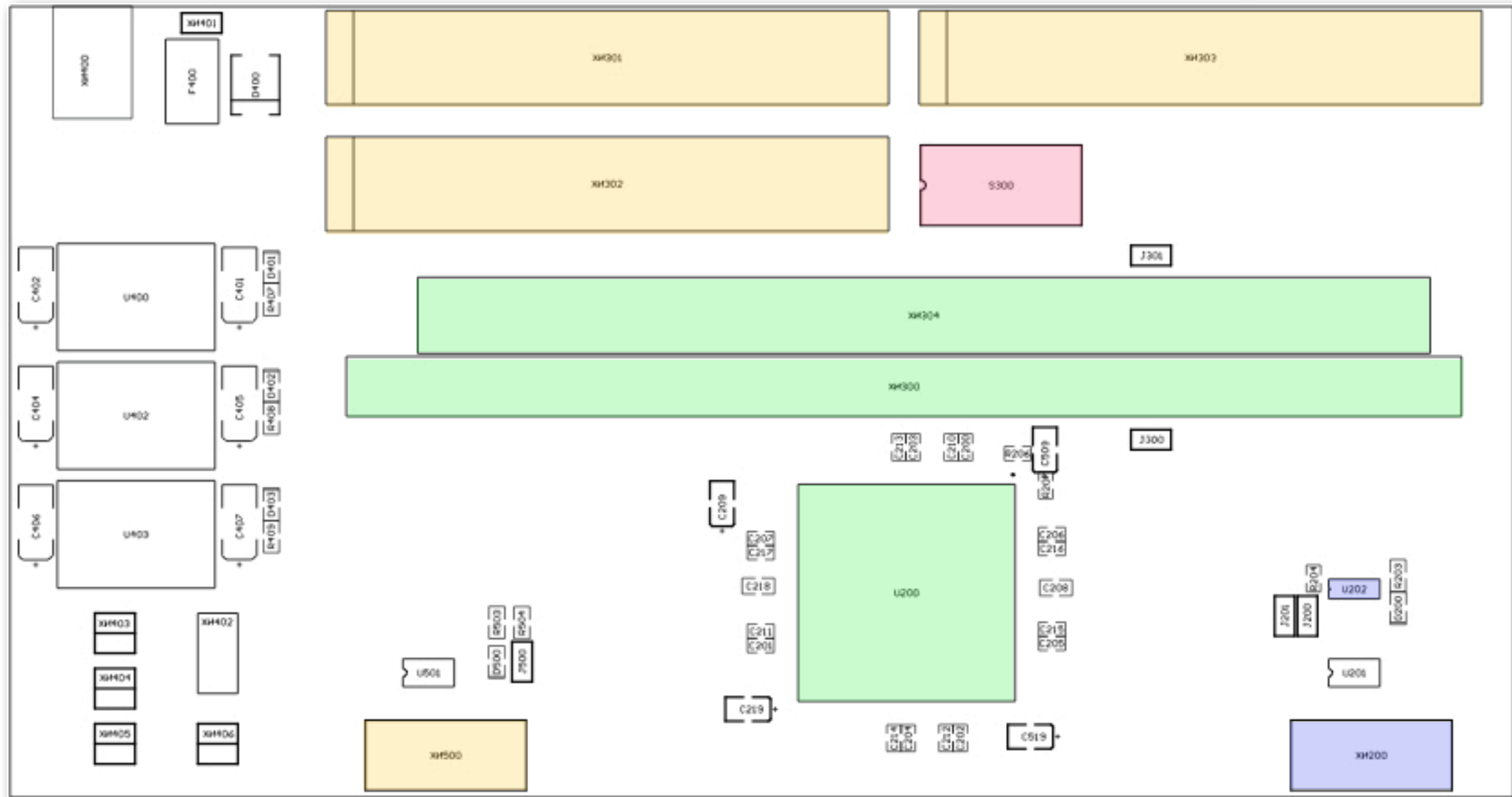
Demo Board Features

- Boundary Scan Device for Memory Access
- FPGA for PI58I Emulation
- Serial PROM for FPGA configuration
- PCI and DIMM Slot for Memory Modules
- Connectors for non-Boundary Scan control
- DIP Switches for Fault Simulation

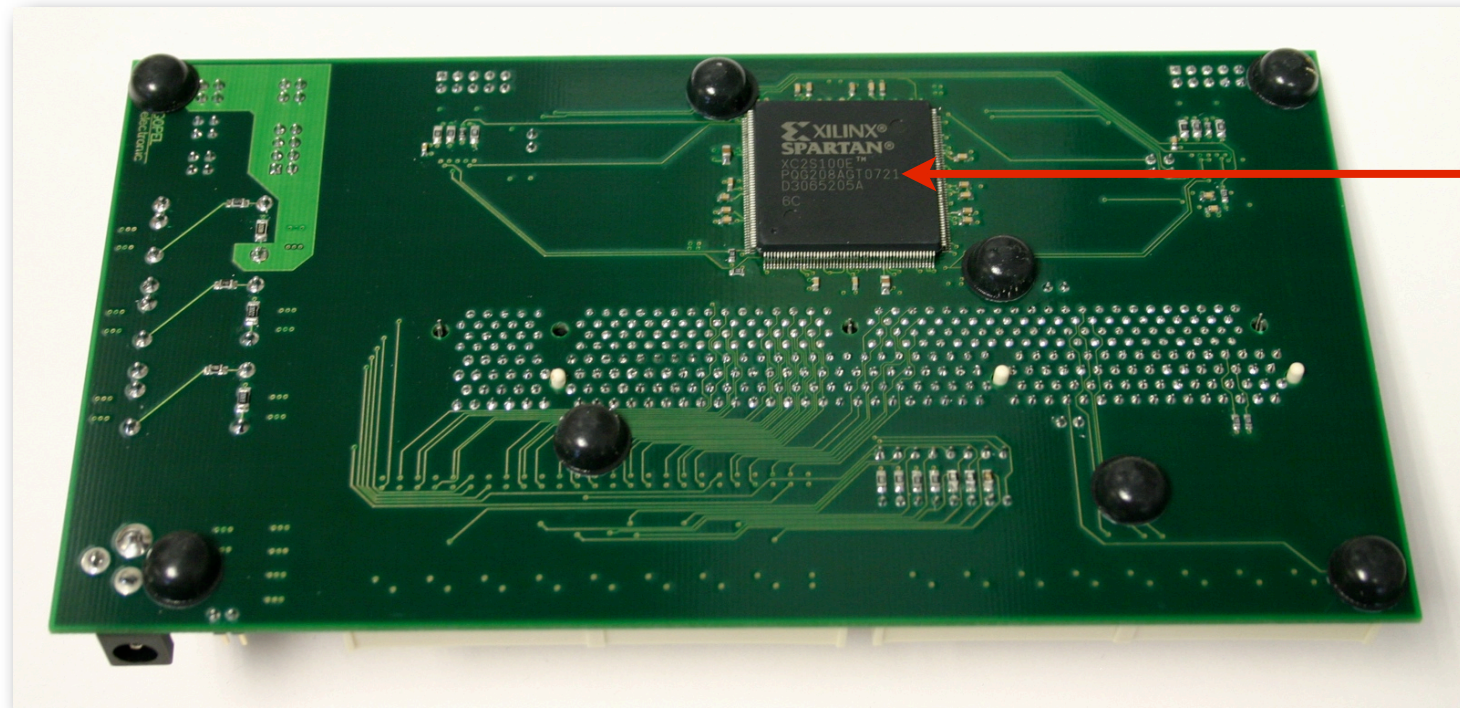
Block Diagram



PI581 Demo Board Layout



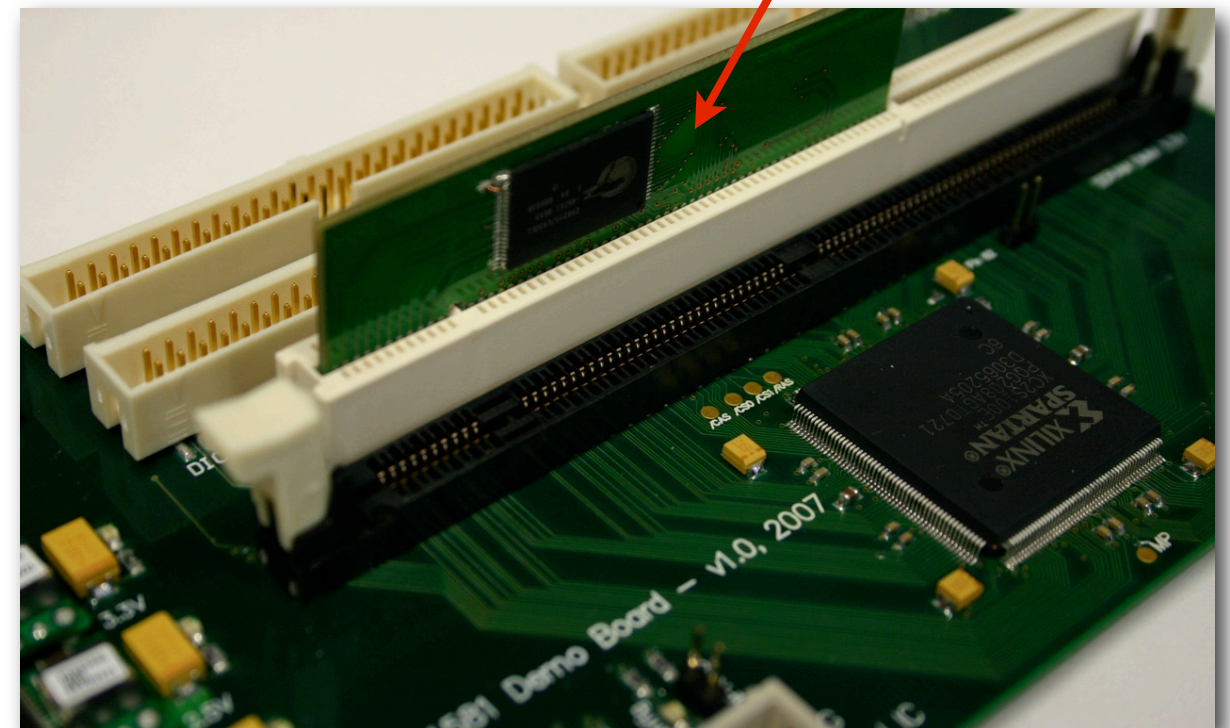
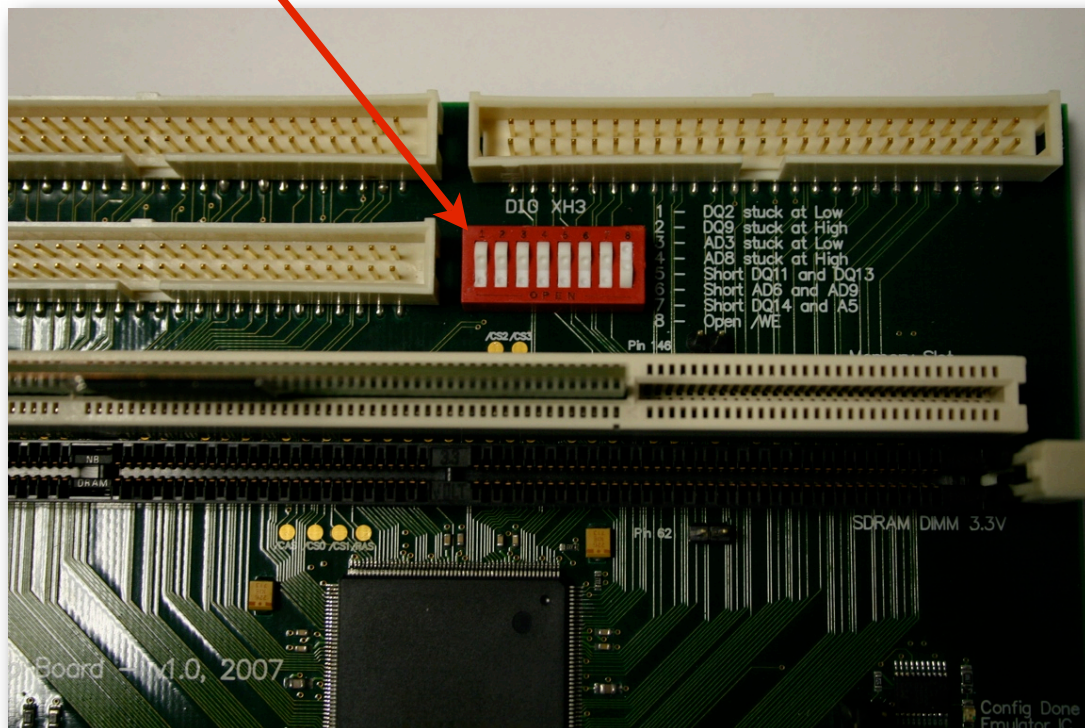
PI581 Demo Board



Memory Controller
(BScan Device)

Memory Module
(holding SRAM)

Fault Switches



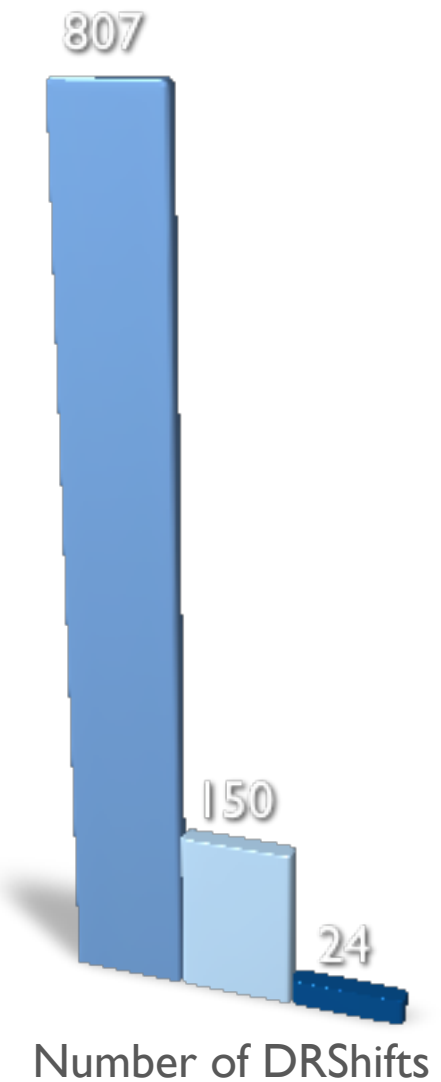
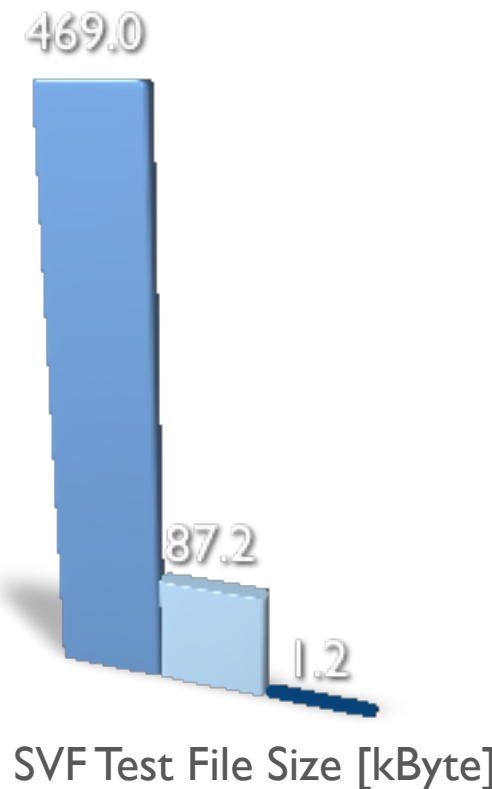
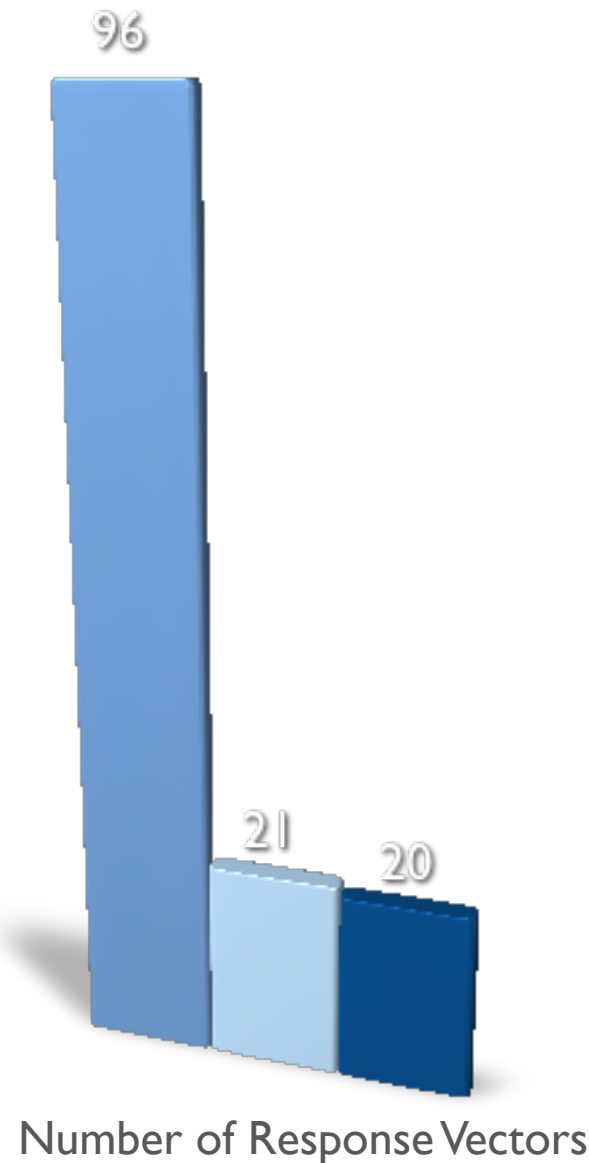
Test applications

- Memory Access Test from Boundary Scan device
- PI58I Continuity Test
- PI58I Device ID verification
- In-System Programming for PI58I Emulator
- Standard Boundary Scan tests for board tests (Infrastructure, Interconnect)

Results

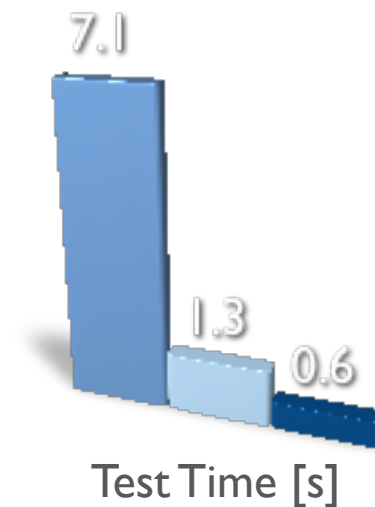
- Test Time
- Test Development (complexity)
- Number of DRShifts
- Test Program Size
- Diagnostics

Results



- BScan Memory Access Test (with Diagnostics)
- BScan Memory Access Test (without Diagnostics)
- PI58I Continuity Test (with Diagnostics)

{Less is better in all four graphs.}



Outlook

- IEEE P1581 provides real benefits
- P1581 Demo Board usable for other Memory Types (e.g. FLASH)
- Demo Board documentation available
- Test Community needs to plead case to Memory Device Manufacturers
- ▶ <http://grouper.ieee.org/groups/1581>